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# Polymer PTC Devices

Surface mount fuses

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**Features** 

LP-NSM050

- □ Very small size of 1206
- ☐ Fast tripping resettable circuit protection
- □ Surface mount packaging for automated assembly
- ☐ Agency recognition: UL、CSA、TUV





### **Product Dimensions (mm)**

Part number -	Α	В	С	D	Е
	Max	Max	Max	Max	Min.
LP-NSM050	3.50	1.80	0.85	0.60	0.20



### **Electrical Characteristics**

Part number -	l <sub>Η</sub>	$I_{T}$	$V_{max}$	I <sub>max</sub>	T <sub>trip</sub>	)	Pd <sub>typ</sub>	$R_{min}$	R <sub>1max</sub>
	(A)	(A)	(V)	(A)	Current(A)	Time(S)	(W)	( )	( )
LP-NSM050	0.50	1.00	6	40	8.0	0.10	0.60	0.15	0.70

I<sub>H</sub>=Hold current: maximum current at which the device will not trip at 25 still air.

I<sub>T</sub>=Trip current: minimum current at which the device will always trip at 25 still air

V<sub>max</sub>=Maximum voltage device can withstand without damage at rated current.

 $I_{\text{max}}$ =Maximum fault current device can withstand without damage at rated voltage.

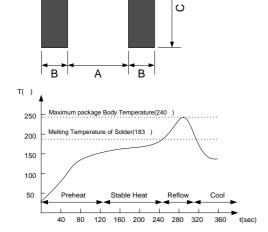
 $T_{trip}$ =Maximum time to trip(s) at assigned current.

Pd<sub>typ</sub>=Typical power dissipation: typical amount of power dissipated by the device when in state air environment.

R<sub>min</sub>=Minimum device resistance at 25 prior to tripping.

 $R_{1\text{max}}\!\!=\!\!\text{Maximum}$  device resistance measured in the nontripped state 1 hour post reflow.

#### **Solder Reflow Recommendations**



## **Solder Pad Layouts**

Part number -	Α	В	С	
Fait number –	(mm)	(mm)	(mm)	
LP-NSM035	1.80	1.00	1.80	

- \* Recommended reflow methods: IR, Vapor phase oven, hot air oven, wave solder.
- \* Devices can be cleaned using standard industry methods and solvents.

#### Notes:

If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

#### Package Information

Tape & Reel: 4000pcs per reel.