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# Polymer PTC Devices

Surface mount fuses

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# Features

LP-SM200

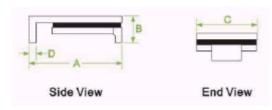
- □ Small size of 1812
- ☐ Fast tripping resettable circuit protection
- □ Surface mount packaging for automated assembly
- ☐ Agency Recognition: UL、CSA、TUV



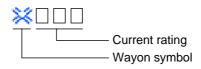


## **Product Dimensions (mm)**

Part number -	Α	В	С	D
	Max	Max	Max	Max
LP-SM200	9.50	3.00	6.71	0.70



# **Part Marking System**



#### **Electrical Characteristics**

Dort number	I <sub>H</sub>	lτ	$V_{max}$	I <sub>max</sub>	$T_{trip}$	)	Pd <sub>typ</sub>	$R_{min}$	R <sub>1max</sub>
Part number	(A)	(A)	(V)	(A)	Current(A)	Time(S)	(W)	( )	( )
LP-SM200	2.00	4.00	15	40	8.0	12.0	2.1	0.045	0.125

 $I_H$ =Hold current: maximum current at which the device will not trip at 25  $\,$  still air.

I<sub>T</sub>=Trip current: minimum current at which the device will always trip at 25 still air.

 $V_{\text{max}}$ =Maximum voltage device can withstand without damage at rated current.

 $I_{\text{max}}$ =Maximum fault current device can withstand without damage at rated voltage.

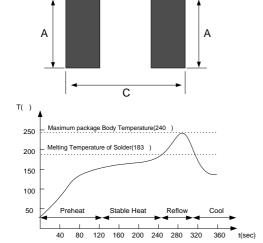
T<sub>trip</sub>=Maximum time to trip(s) at assigned current.

Pd<sub>typ</sub>=Typical power dissipation: typical amount of power dissipated by the device when in state air environment.

R<sub>min</sub>=Minimum device resistance at 25 prior to tripping.

 $R_{1\text{max}}$ =Maximum device resistance measured in the nontripped state 1 hour post reflow.

## **Solder Reflow Recommendations**



#### **Solder Pad Layouts**

Part number -	Α	В	С
Part number	(mm)	(mm)	(mm)
LP-SM200	4.6	2.3	10.7

- \* Recommended reflow methods: IR, Vapor phase oven, hot air oven, wave solder.
- \* Devices can be cleaned using standard industry methods and solvents.

#### Notes:

If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

## **Package Information**

Tape & Reel: 2000pcs per reel.