LP-SM110

Polymer
PTC Devices
Surface mount fuses

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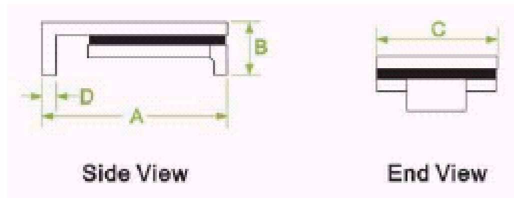
Features

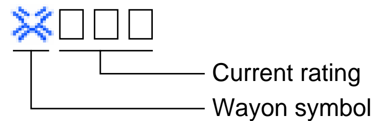
- Small size of 1812
- Fast tripping resettable circuit protection
- Surface mount packaging for automated assembly
- Agency Recognition: UL、CSA、TUV

Product Dimensions (mm)

Part number	A	B	C	D
	Max	Max	Max	Max
LP-SM110	7.98	3.00	5.44	0.70



Part Marking System

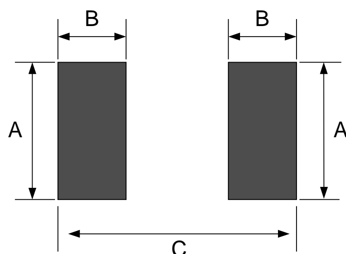


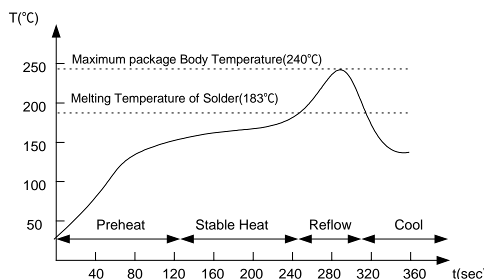
Electrical Characteristics

Part number	I_H	I_T	V_{max}	I_{max}	T_{trip}		Pd_{typ}	R_{min}	R_{1max}
	(A)	(A)	(V)	(A)	Current(A)	Time(S)	(W)	(Ω)	(Ω)
LP-SM110	1.10	2.20	33	40	8.0	2.0	1.9	0.100	0.480

I_H=Hold current: maximum current at which the device will not trip at 25°C still air.
I_T=Trip current: minimum current at which the device will always trip at 25°C still air.
V_{max}=Maximum voltage device can withstand without damage at rated current.
I_{max}=Maximum fault current device can withstand without damage at rated voltage.
T_{trip}=Maximum time to trip(s) at assigned current.
Pd_{typ}=Typical power dissipation: typical amount of power dissipated by the device when in state air environment.
R_{min}=Minimum device resistance at 25°C prior to tripping.
R_{1max}=Maximum device resistance measured in the nontripped state 1 hour post reflow.

Solder Reflow Recommendations





Solder Pad Layouts

Part number	A	B	C
	(mm)	(mm)	(mm)
LP-SM110	3.1	2.3	9.7

* Recommended reflow methods: IR, Vapor phase oven, hot air oven, wave solder.
* Devices can be cleaned using standard industry methods and solvents.

Notes:
If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

Package Information

Tape & Reel: 2000pcs per reel.